GYAN SAGAR COLLEGE OF ENGINEERING
New Scheme Based On AICTE Flexible Curricula
Electrical & Electronics Engineering, IV-Semester

Digital Electronics Logic Design Lab.

LIST OF EXPERIMENTS

1. Verification of all the logicgates.

2. Design of BCD to Excess-3 codeconverter.

3. Implementation of NAND & NOR as Universalgate.

4. Design of RS, JK, T& D Flipflop.

5. Multiplexer /Demultipexer based boolean function

6. Design of combinational circuit forthe

(i) Halfadder

(ii) Fulladder

(iii) Half subtractor

(iv) Fullsubtractor

7. Design various A-D & D-Aconvertors.

8. Verify the truth table of SR flip flop

9. Verify BCD to seven segment decoder